

Type SF73-xx20 High Frequency Inverter grade Capsule Thyristor

Distributed amplified gate for high di/dt and low switching losses

Maximum mean on-state current							I_{TAV}	2000 A	
Maximum repetitive peak off-state and reverse voltage							U_{DRM}	1200 ÷ 2200 V	
Turn-off time							U_{RRM}		
							tq	32; 40; 50 μs	
U_{DRM}, U_{RRM}, V	1200	1400	1600	1800	2000	2200			
Voltage code - XX	12	14	16	18	20	22			
$T_{vj}, ^\circ C$	- 60 ÷ 125								

MAXIMUM ALLOWABLE RATINGS

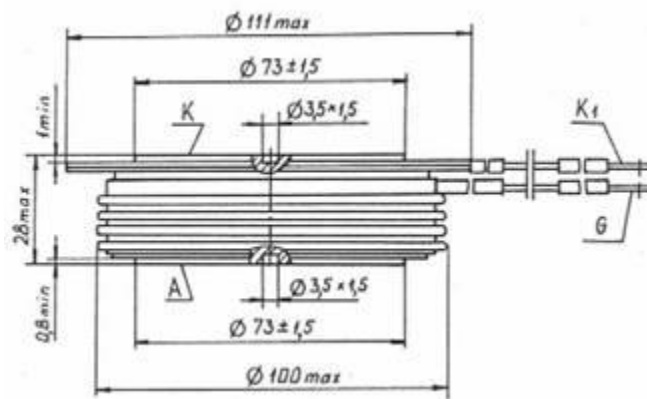
Symbols and parameters		Units	SF73-xx20	Conditions
I_{TAV}	Mean on-state current	A	2000 2890	$T_c=85^\circ C$, $T_c=55^\circ C$, 180° half-sine wave, 50 Hz
I_{TRMS}	RMS on-state current	A	3140	$T_c=80^\circ C$, 50 Hz
I_{TSM}	Surge on-state current	kA	40	$T_{vj}=125^\circ C$ $U_R=0$ $t_p=10$ ms
(di/dt) cr	Critical rate of rise of on-state current: non – repetitive repetitive	A/ μ s	1600 1000	$T_{vj}=125^\circ C$; $U_D=0,67 U_{DRM}$, Gate pulse : 10V,5 Ω , 1 μ s rise time, 10 μ s
U_{RGM}	Peak reverse gate voltage	V	5	
T_{stg}	Storage temperature	$^\circ C$	-60 ÷ 125	
T_{vj}	Junction temperature	$^\circ C$	-60 ÷ 125	

CHARACTERISTICS

Symbols and parameters		Units	SF73xx20	Conditions
U_{TM}	Peak on-state voltage	V	2,2	$T_{vj}=25^\circ C$, $I_{TM}=3,14 I_{TAV}$
$U_{T(To)}$	Threshold voltage	V	1,3	$T_{vj}=125^\circ C$
r_T	Slope resistance	m Ω	0,125	$T_{vj}=125^\circ C$
I_{DRM} I_{RRM}	Repetitive peak off-state and reverse current	mA	150 150	$T_{vj}=125^\circ C$, $U_D= U_{DRM}$ $U_R= U_{RRM}$
I_H	Holding current	A	1,0	$T_{vj}=25^\circ C$; $U_D=12$, Gate open
U_{GT}	Gate trigger direct voltage	V	2,5	$T_{vj}=25^\circ C$; $U_D=12V$

IGT	Gate trigger direct current	A	0,35	Tvj=25°C; UD=12V
UGD	Gate non-trigger direct voltage	V	0,25	Tvj=125°C; UD=0,67 U _{DRM}
tgd	Delay time	μs	2,5	Tvj=25°C, UD=500V, I _{TM} =2000A Gate pulse: 10V, 5Ω, 1μs rise time, 10μs
tgt	Turn-on time	μs	4,0	
tq	Turn-off time	μs	32÷50 40÷63	Tvj=125°C, I _{TM} =2000A, di _R /dt= 10 A/μs UR=100V UD=0,67 U _{DRM} Di _D /dt= 50 A/μs Di _D /dt= 200 A/μs
Qrr	Recovered charge	μC	800	Tvj=125°C, I _{TM} =2000A, di _R /dt= 50 A/μs, UR=100V
(di _D /dt) cr	Critical rate of rise of off-state voltage	V/μs	500 1000	Tvj=125°C; UD=0,67 U _{DRM} Gate open
Rthjc	Thermal resistance junction to case	°C/W	0,011	Direct current, double side cooled

Mounting force : 36 ÷ 46 kN
Weight : 1200 gram



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